

IN THE CLAIMS

What is claimed is:

1. (Currently Amended) A method of debugging a system by analyzing transactions of a serial intra-system bus comprising the steps of:
 - capturing frames of a serial intra-system bus in a capture data file;
 - extracting frames from the capture data file;
 - checking frames for out-of-bounds addresses;
 - decoding an address of frames to identify a particular slave device;
 - identifying a computer model of the particular slave device in a library of device models of slave devices,
 - tracking state changes indicated in frames with a the computer model of the slave device; and
 - recording state error information when state changes indicated in frames are not permissible state changes of the computer model.
2. (Original) The method of Claim 1, further comprising the step of assembling packets that are encapsulated in the frames.
3. (Original) The method of Claim 2, further comprising the step of validating assembled packets.
4. (Original) The method of Claim 3, further comprising the step of determining if the state changes are permissible state changes.
5. (Original) The method of Claim 4, wherein the step of determining if the state changes are permissible state changes is performed by tracking state changes indicated in the assembled packets in a protocol model, and comparing state changes against permitted state changes of the model.
6. (Currently Amended) A memory device containing machine readable code for analyzing captured frames from a serial intra-system bus, the machine readable code comprising a library of computer models of slave devices and code for:
 - extracting frames from the capture data file;
 - checking frames for out-of-bounds addresses;

decoding an address of frames to identify a particular slave device;
tracking state changes indicated in frames with a computer model of the slave device, the computer model of the slave device being extracted from the library of computer models of slave devices; and
recording state error information when state changes indicated in frames are not permissible state changes of the computer model.

7. (Original) The method of Claim 6, further comprising the step of assembling packets that are encapsulated in the frames.

8. (Original) The method of Claim 7, further comprising the step of validating assembled packets.

9. (Original) The method of Claim 8, further comprising the step of determining if the state changes are permissible state changes.

10. (Original) The method of Claim 9, wherein the step of determining if the state changes are permissible state changes is performed by tracking state changes indicated in the assembled packets in a protocol model, and comparing state changes against permitted state changes of the model.